



Docket No.: MAS-FIN-410

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By: Wm Stemmer

Date: August 24, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/698,081 Confirmation No: 9173  
Applicant : Rudolf Lehner  
Filed : October 30, 2003  
Title : Electronic Component having a Semiconductor Chip and  
Method for Populating a Circuit Carrier During the Production of  
the Electronic Component  
Art Unit : 2815  
Examiner : Jasmine Jhihan B. Clark  
  
Docket No. : MAS-FIN-410  
Customer No. : 24131

RESPONSE

Hon. Commissioner for Patents

Sir:

Responsive to the Office Action dated August 18, 2004, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicant elects group I, claims 1-9, for prosecution at this time.

Applicant requests a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-9 is solicited.

Respectfully submitted,

Wm Stemmer

For Applicant

WERNER H. STEMER  
REG. NO. 34,956

Date: August 24, 2004

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